

1. General description

Planar passivated high commutation three quadrant triac in a TO263 (D2PAK) surface mountable plastic package. This "series ET" triac balances the requirements of commutation performance and gate sensitivity and is intended for interfacing with low power drivers including microcontrollers. It is used in applications where "high junction operating temperature" capability is required.

2. Features and benefits

- 3Q technology for improved noise immunity
- Direct interfacing with low power drivers and microcontrollers
- Good immunity to false turn-on by dV/dt
- High commutation capability with sensitive gate
- High junction operating temperature capability
- High voltage capability
- Planar passivated for voltage ruggedness and reliability
- Sensitive gate for easy logic level triggering
- Surface mountable package
- Triggering in three quadrants only

3. Applications

- Applications subject to high temperature
- Electronic thermostats (heating and cooling)
- High power motor controls e.g. washing machines and vacuum cleaners
- Refrigeration and air-conditioner compressor controls

4. Quick reference data

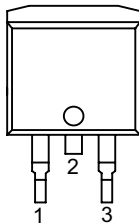
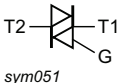
Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	-	800	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{mb} \leq 125\text{ °C}$; Fig. 1 ; Fig. 2 ; Fig. 3	-	-	12	A
I_{TSM}	non-repetitive peak on-state current	full sine wave; $T_{j(imit)} = 25\text{ °C}$; $t_p = 20\text{ ms}$; Fig 4 ; Fig 5	-	-	100	A
		full sine wave; $T_{j(imit)} = 25\text{ °C}$; $t_p = 16.7\text{ ms}$	-	-	110	A
T_j	junction temperature		-	-	150	°C
Static characteristics						

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I_{GT}	gate trigger current	$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T_2+ G+;$ $T_j = 25\text{ }^\circ\text{C};$ Fig. 7	-	-	10	mA
		$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T_2+ G-;$ $T_j = 25\text{ }^\circ\text{C};$ Fig. 7	-	-	10	mA
		$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T_2- G-;$ $T_j = 25\text{ }^\circ\text{C};$ Fig. 7	-	-	10	mA
I_H	holding current	$V_D = 12\text{ V}; T_j = 25\text{ }^\circ\text{C};$ Fig. 9	-	-	15	mA
V_T	on-state voltage	$I_T = 15\text{ A}; T_j = 25\text{ }^\circ\text{C};$ Fig. 10	-	1.3	1.6	V
Dynamic characteristics						
dV_D/dt	rate of rise of off-state voltage	$V_{DM} = 536\text{ V}; T_j = 150\text{ }^\circ\text{C}; (V_{DM} = 67\%$ of V_{DRM}); exponential waveform; gate open circuit	30	-	-	V/ μ s
dI_{com}/dt	rate of change of commutating current	$V_D = 400\text{ V}; T_j = 150\text{ }^\circ\text{C}; I_{T(RMS)} = 12\text{ A};$ $dV_{com}/dt = 20\text{ V}/\mu\text{s};$ (snubberless condition); gate open circuit	2	-	-	A/ms
		$V_D = 400\text{ V}; T_j = 150\text{ }^\circ\text{C}; I_{T(RMS)} = 12\text{ A};$ $dV_{com}/dt = 10\text{ V}/\mu\text{s};$ gate open circuit	3.5	-	-	A/ms
		$V_D = 400\text{ V}; T_j = 150\text{ }^\circ\text{C}; I_{T(RMS)} = 12\text{ A};$ $dV_{com}/dt = 1\text{ V}/\mu\text{s};$ gate open circuit	5	-	-	A/ms

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1		 sym051
2	T2	main terminal 2		
3	G	gate		
mb	T2	mounting base; main terminal 2		

6. Ordering information

Table 3. Ordering information

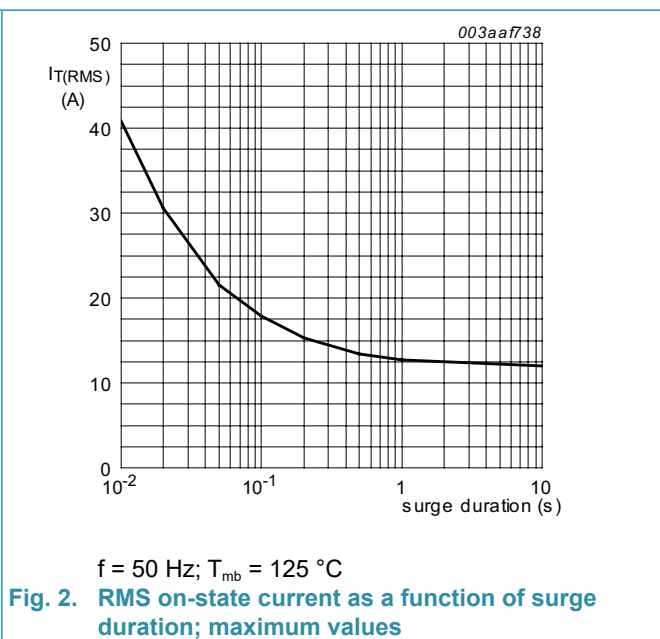
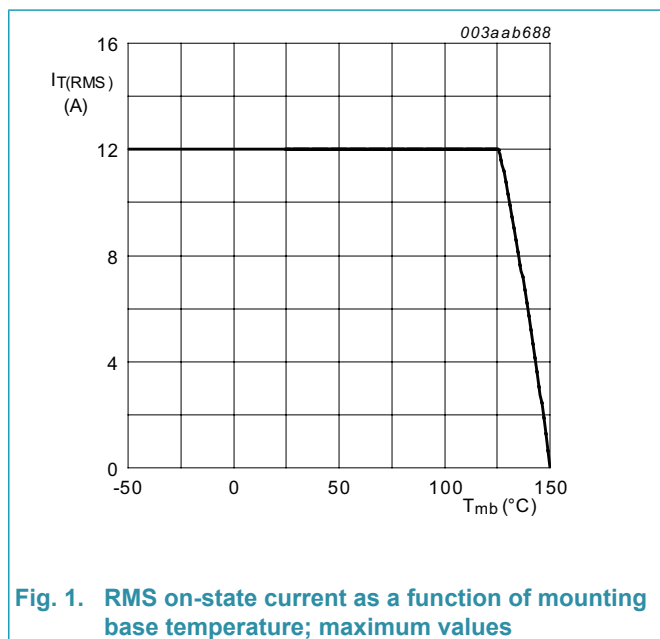
Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
BTA312B-800ET	TO263	BTA312B-800ETJ	Reel	800	TO263E	26-May-2017

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	800	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{mb} \leq 125\text{ °C}$; Fig. 1 ; Fig. 2 ; Fig. 3	-	12	A
I_{TSM}	non-repetitive peak on-state current	full sine wave; $T_{j(init)} = 25\text{ °C}$; $t_p = 20\text{ ms}$; Fig 4 ; Fig 5	-	100	A
		full sine wave; $T_{j(init)} = 25\text{ °C}$; $t_p = 16.7\text{ ms}$	-	110	A
I^2t	I^2t for fusing	$t_p = 10\text{ ms}$; SIN	-	50	A^2s
di_T/dt	rate of rise of on-state current	$I_G = 20\text{ mA}$	-	100	$A/\mu s$
I_{GM}	peak gate current		-	2	A
P_{GM}	peak gate power		-	5	W
$P_{G(AV)}$	average gate power	over any 20 ms period	-	0.5	W
T_{stg}	storage temperature		-40	150	$^{\circ}C$
T_j	junction temperature		-	150	$^{\circ}C$



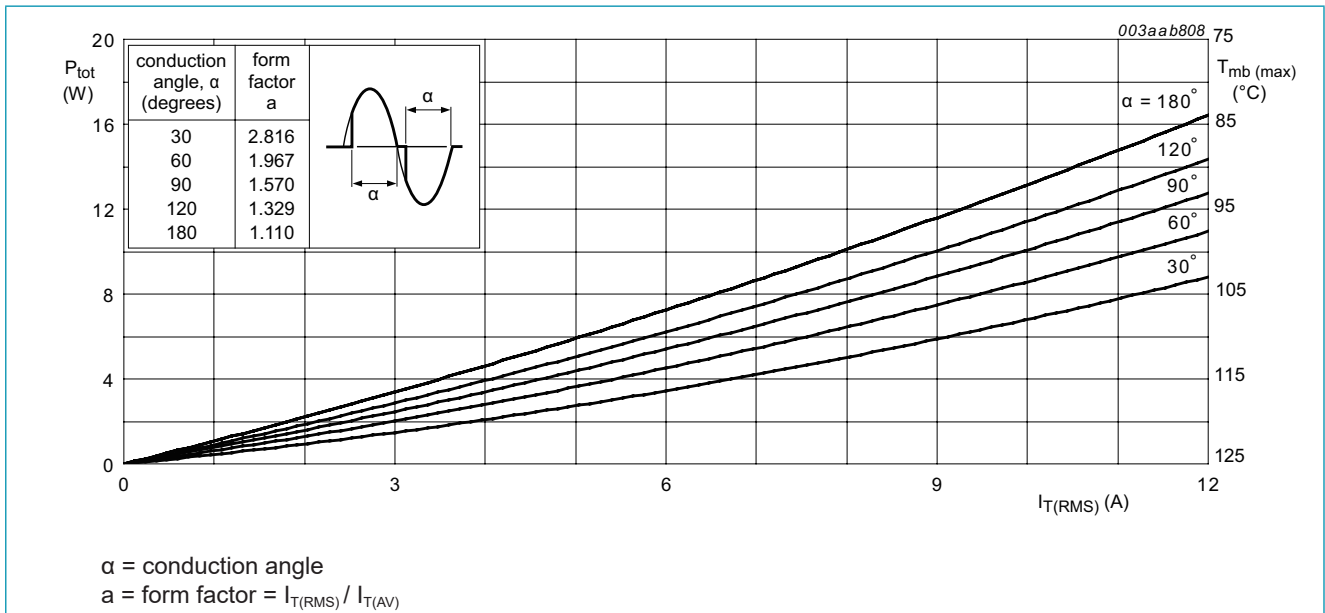


Fig. 3. Total power dissipation as a function of RMS on-state current; maximum values

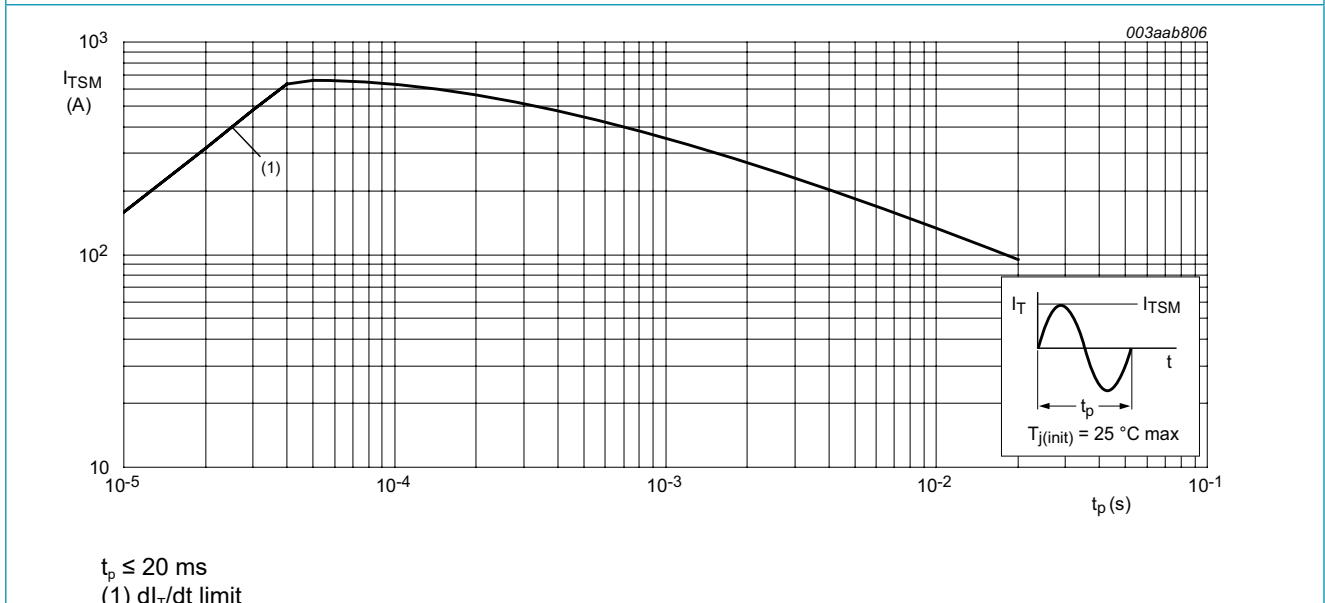
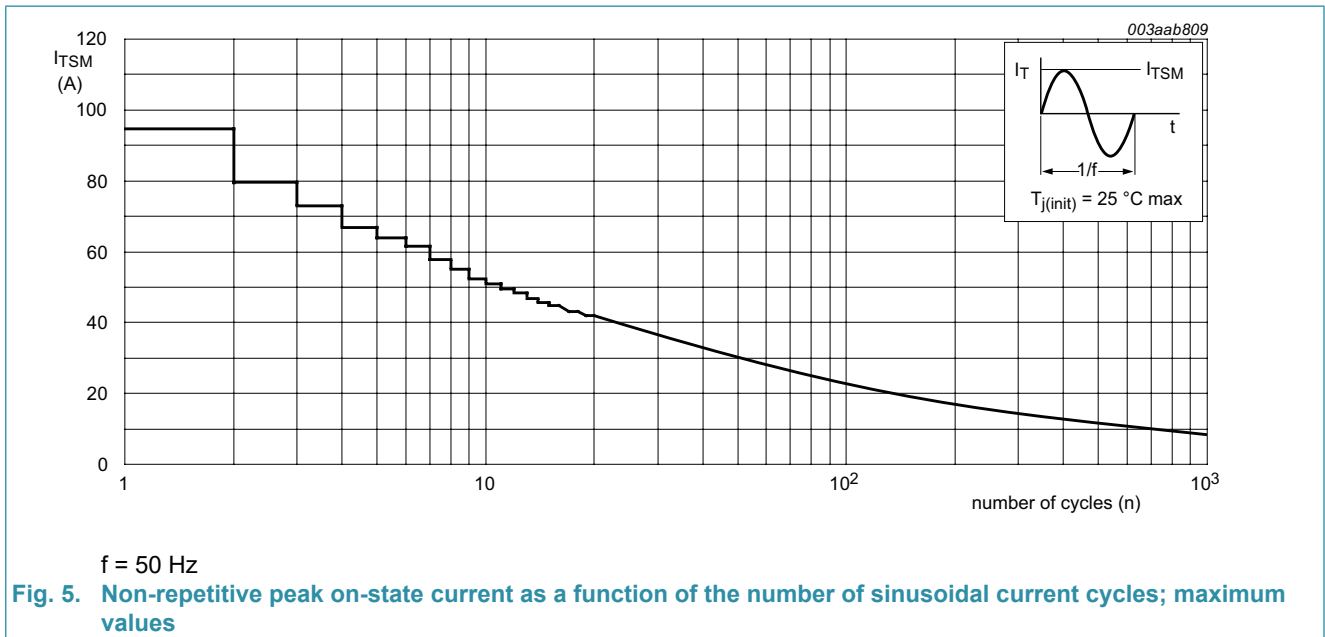


Fig. 4. Non-repetitive peak on-state current as a function of pulse duration; maximum values



8. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	full cycle; Fig 6	-	-	1.5	K/W
		half cycle; Fig 6	-	-	2	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient free air	printed circuit board mounted; minimum footprint	-	55	-	K/W

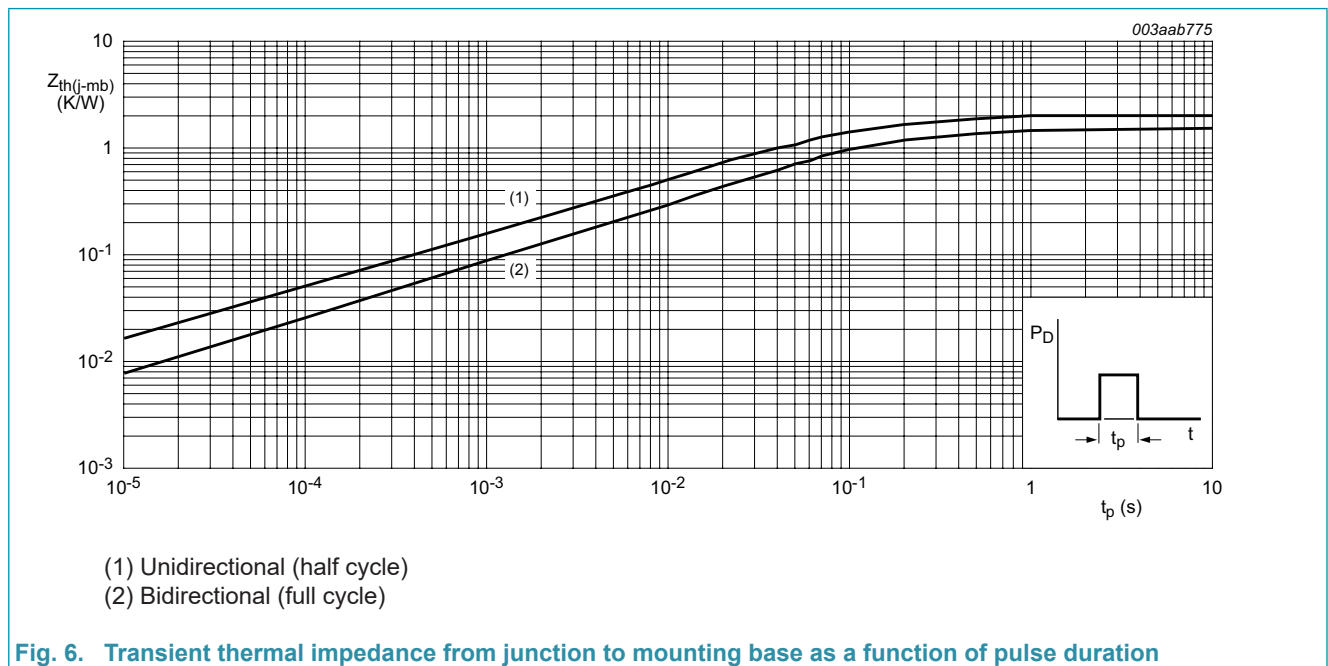
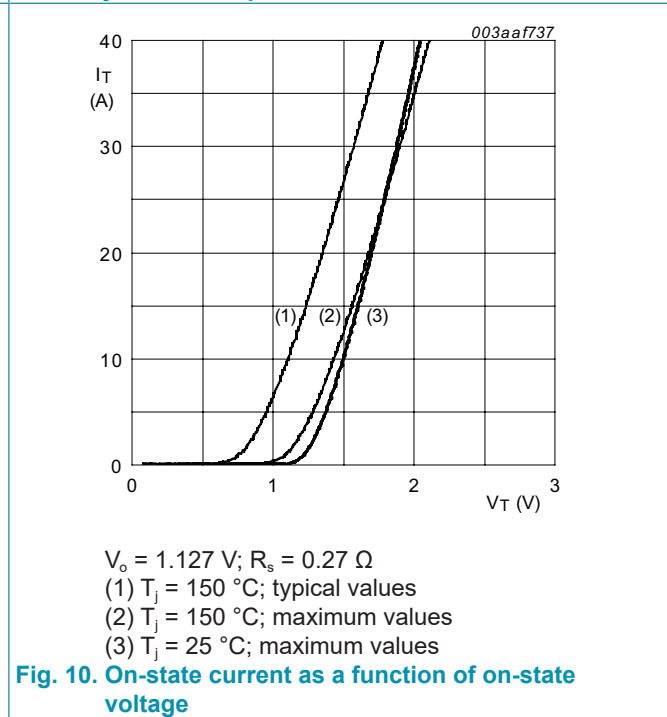
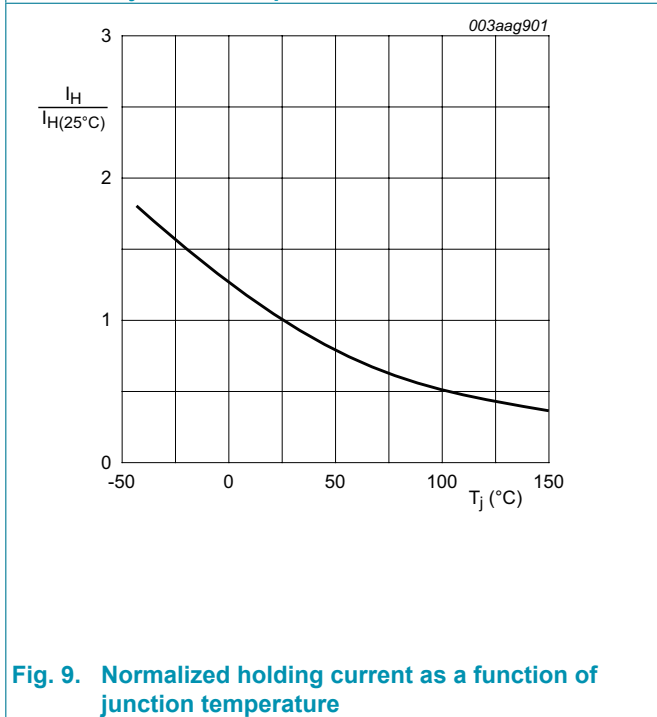
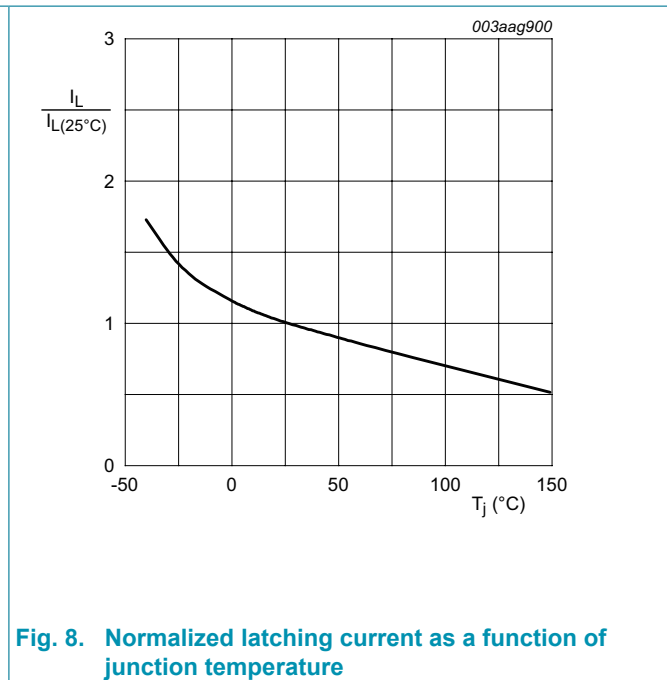
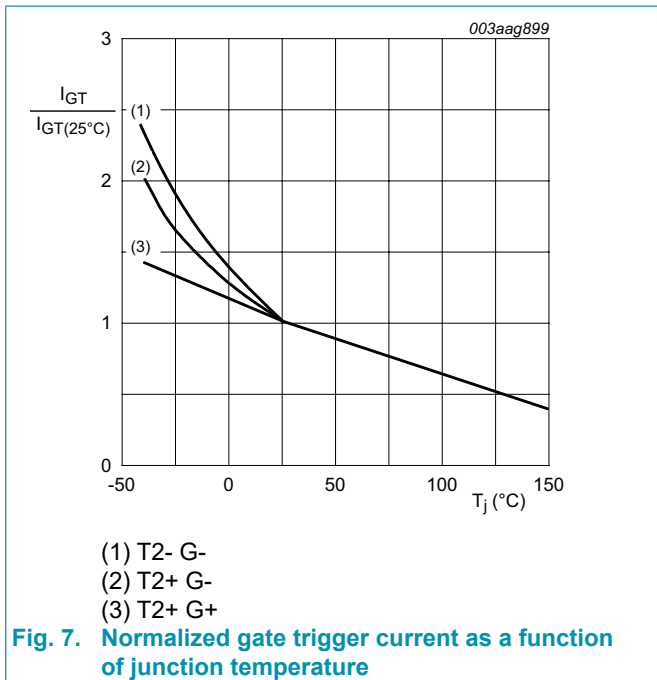


Fig. 6. Transient thermal impedance from junction to mounting base as a function of pulse duration

9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
I_{GT}	gate trigger current	$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T_2+ G+;$ $T_J = 25\text{ °C};$ Fig. 7	-	-	10	mA
		$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T_2+ G-;$ $T_J = 25\text{ °C};$ Fig. 7	-	-	10	mA
		$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T_2- G-;$ $T_J = 25\text{ °C};$ Fig. 7	-	-	10	mA
I_L	latching current	$V_D = 12\text{ V}; I_G = 0.1\text{ A}; T_2+ G+;$ $T_J = 25\text{ °C};$ Fig. 8	-	-	25	mA
		$V_D = 12\text{ V}; I_G = 0.1\text{ A}; T_2+ G-;$ $T_J = 25\text{ °C};$ Fig. 8	-	-	30	mA
		$V_D = 12\text{ V}; I_G = 0.1\text{ A}; T_2- G-;$ $T_J = 25\text{ °C};$ Fig. 8	-	-	25	mA
I_H	holding current	$V_D = 12\text{ V}; T_J = 25\text{ °C};$ Fig. 9	-	-	15	mA
V_T	on-state voltage	$I_T = 15\text{ A}; T_J = 25\text{ °C};$ Fig. 10	-	1.3	1.6	V
V_{GT}	gate trigger voltage	$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T_J = 25\text{ °C};$ Fig. 11	-	0.7	1	V
		$V_D = 400\text{ V}; I_T = 0.1\text{ A}; T_J = 125\text{ °C};$ Fig. 11	0.25	0.4	-	V
I_D	off-state current	$V_D = 800\text{ V}; T_J = 125\text{ °C}$	-	0.4	2	mA
Dynamic characteristics						
dV_D/dt	rate of rise of off-state voltage	$V_{DM} = 536\text{ V}; T_J = 150\text{ °C}; (V_{DM} = 67\%$ of $V_{DRM});$ exponential waveform; gate open circuit	30	-	-	V/ μ s
dI_{com}/dt	rate of change of commutating current	$V_D = 400\text{ V}; T_J = 150\text{ °C}; I_{T(RMS)} = 12\text{ A};$ $dV_{com}/dt = 20\text{ V}/\mu\text{s};$ (snubberless condition); gate open circuit	2	-	-	A/ms
		$V_D = 400\text{ V}; T_J = 150\text{ °C}; I_{T(RMS)} = 12\text{ A};$ $dV_{com}/dt = 10\text{ V}/\mu\text{s};$ gate open circuit	3.5	-	-	A/ms
		$V_D = 400\text{ V}; T_J = 150\text{ °C}; I_{T(RMS)} = 12\text{ A};$ $dV_{com}/dt = 1\text{ V}/\mu\text{s};$ gate open circuit	5	-	-	A/ms



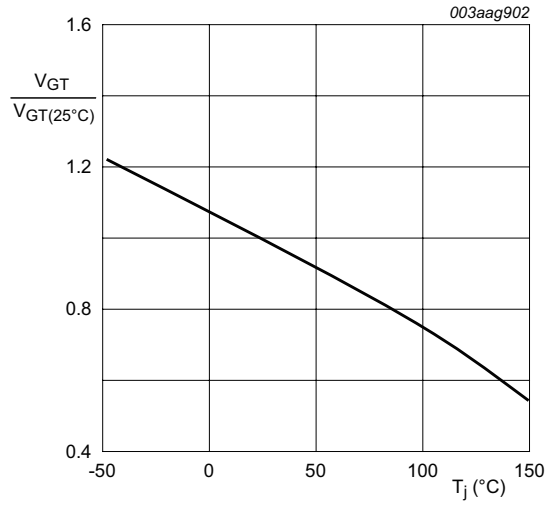


Fig. 11. Normalized gate trigger voltage as a function of junction temperature

10. Package outline

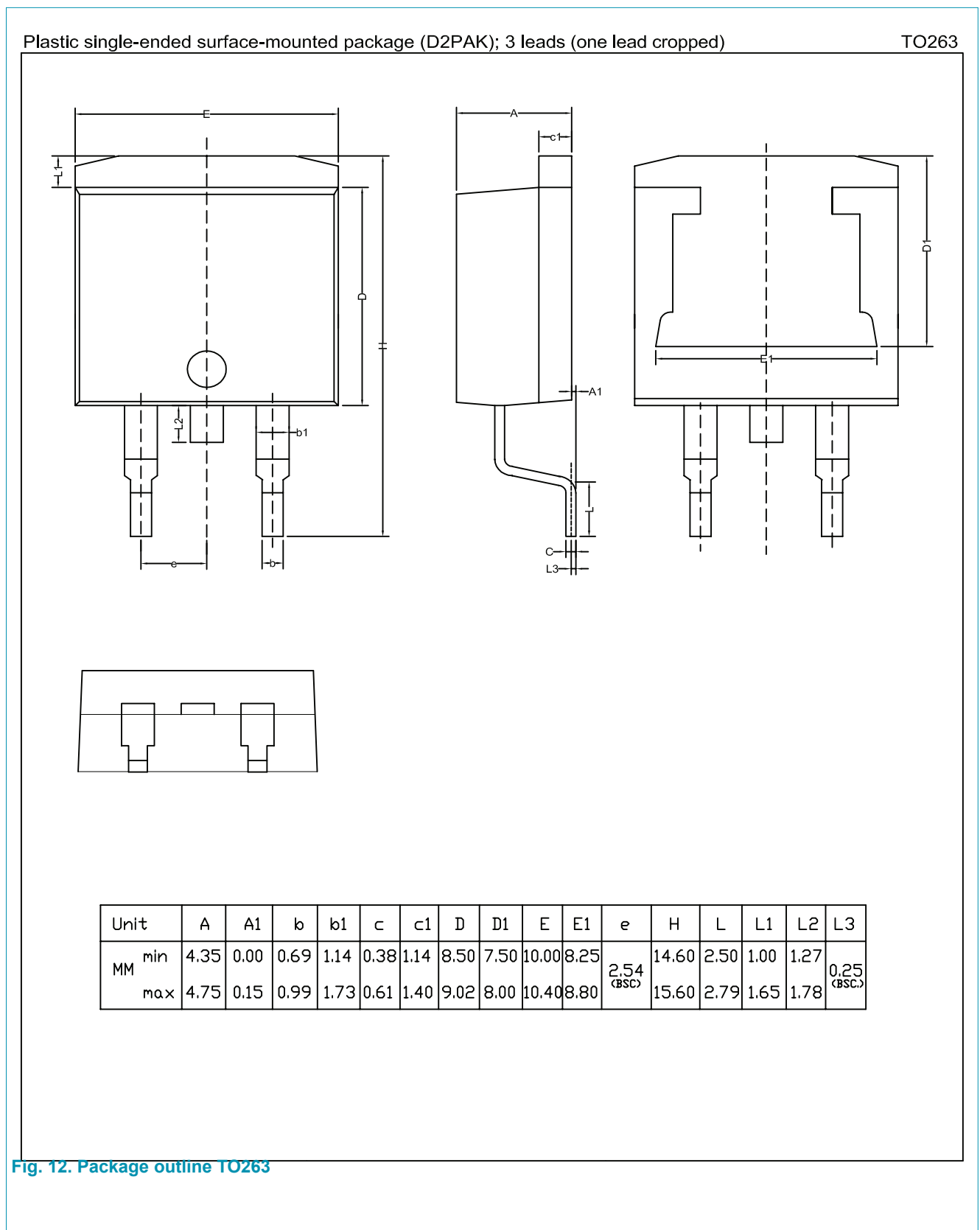


Fig. 12. Package outline TO263

11. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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For sales office addresses, please send an email to: salesaddresses@ween-semi.com
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