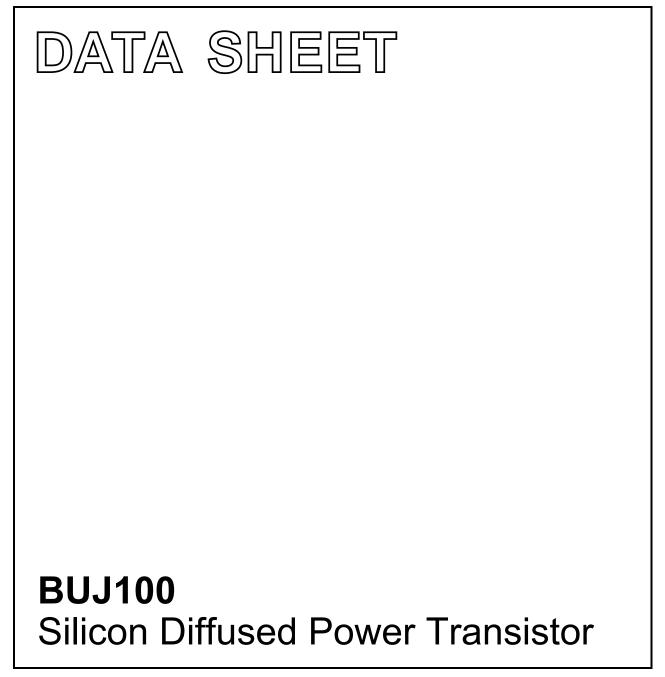
DISCRETE SEMICONDUCTORS



Product specification

September 2018



## BUJ100

## **GENERAL DESCRIPTION**

High-voltage, high-speed planar-passivated npn power switching transistor in the TO92 envelope intended for use in compact fluorescent lamps and low power electronic lighting ballasts, converters and inverters, etc.

## QUICK REFERENCE DATA

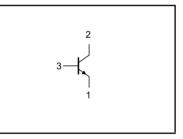
SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
$V_{CESM} \\ V_{CBO} \\ V_{CEO} \\ I_C \\ I_{CM} \\ P_{tot} \\ V_{CEsat} $	Collector-emitter voltage peak value Collector-Base voltage (open emitter) Collector-emitter voltage (open base) Collector current (DC) Collector current peak value Total power dissipation Collector-emitter saturation voltage	$V_{BE} = 0 V$ $T_{lead} \le 25 \degree C$ $I_{C} = 0.75 A; I_{B} = 150 mA$ $I_{C} = 0.75 A; V_{CE} = 5 V$	- - - - - - 0.24	700 700 400 1.0 2.0 2 1.0	> > > > > > > > > > > > > > > > > > >
h <sub>FE</sub> t <sub>fi</sub>	Fall time (Inductive)	I <sub>C</sub> = 0.75 A;V <sub>CE</sub> = 5 V  I <sub>C</sub> = 1.0 A;I <sub>BON</sub> = 200mA	14 50	20 70	ns

### PINNING - TO92

PIN	DESCRIPTION	
1	Emitter	
2	Collector	
3	Base	

## **PIN CONFIGURATION**

#### SYMBOL



## LIMITING VALUES

Limiting values in accordance with the Absolute Maximum Rating System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CESM</sub>	Collector to emitter voltage	$V_{BE} = 0 V$	-	700	V
V <sub>CEO</sub>	Collector to emitter voltage (open base)		-	400	V
V <sub>CBO</sub>	Collector to base voltage (open emitter)		-	700	V
I <sub>c</sub>	Collector current (DC)		-	1.0	A
I <sub>CM</sub>	Collector current peak value		-	2.0	A
I <sub>B</sub>	Base current (DC)		-	0.5	A
I <sub>BM</sub>	Base current peak value		-	1.0	Α
P <sub>tot</sub>	Total power dissipation	T <sub>lead</sub> ≤ 25 °C	-	2	W
T <sub>stq</sub>	Storage temperature		-65	150	°C
$ T_i $	Junction temperature		-	150	°C

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## THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
$R_{thj-lead}$	Thermal resistance junction to lead		-	60	K/W
R <sub>th j-a</sub>	Thermal resistance Junction to ambient	pcb mounted; lead length = 4mm	150	-	K/W

# Silicon Diffused Power Transistor

# BUJ100

## STATIC CHARACTERISTICS

T <sub>lead</sub> = 25 °C unless oth	nerwise specified
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SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>ces</sub> ,I <sub>cbo</sub> I <sub>ces</sub>	Collector cut-off current <sup>1</sup>	$V_{BE} = 0 V; V_{CE} = V_{CESMmax}$ $V_{BE} = 0 V; V_{CE} = V_{CESMmax};$ $T_i = 125 °C$	-	0.8 2.0	100 500	μΑ μΑ
I <sub>CEO</sub> I <sub>EBO</sub> V <sub>CEOsust</sub>	Collector cut-off current Emitter cut-off current Collector-emitter sustaining voltage	$V_{CEO} = V_{CEOMmax}(400V)$ $V_{EB} = 9 V; I_{C} = 0 A$ $I_{B} = 0 A; I_{C} = 10mA;$ L = 25  mH	- - 400	- 0.05 -	100 100 -	μΑ μΑ V
V <sub>CEsat</sub> V <sub>BEsat</sub>	Collector-emitter saturation voltage Base-emitter saturation voltage	$I_{\rm C} = 0.75 \text{ A}; I_{\rm B} = 0.15 \text{ A}$ $I_{\rm C} = 0.75 \text{ A}; I_{\rm B} = 0.15 \text{ A}$	-	0.24 0.93	1.0 1.3	V V
h <sub>FE</sub> h <sub>FE</sub> h <sub>FE</sub>	DC current gain	$      I_{\rm C} = 10 m {\rm A};  V_{\rm CE} = 5  {\rm V} \\       I_{\rm C} = 100 m {\rm A};  V_{\rm CE} = 5  {\rm V} \\       I_{\rm C} = 0.75  {\rm A};  V_{\rm CE} = 5  {\rm V} $	11 12.5 9	20 21 14	27 31 20	

## **DYNAMIC CHARACTERISTICS**

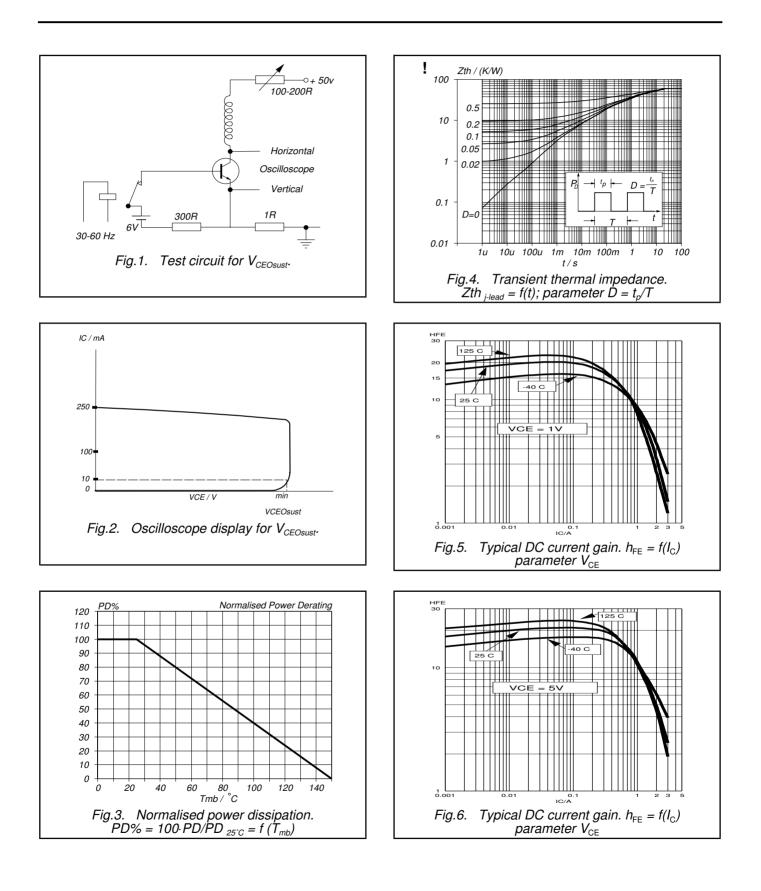
 $T_{lead} = 25$  °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
	Switching times (resistive load)	$I_{Con} = 1.0 \text{ A}; I_{Bon} = -I_{Boff} = 200 \text{ mA};$ R <sub>1</sub> = 75 ohms; V <sub>BB2</sub> = 4 V;			
t <sub>on</sub>	Turn-on time		0.65	0.88	μs
ts	Turn-off storage time		0.88	1.2	μs
t <sub>f</sub>	Turn-off fall time		250	338	ns
	Switching times (inductive load)	$I_{Con} = 1.0 \text{ A}; I_{Bon} = 200 \text{ mA}; L_{B} = 1  \mu\text{H};$ - $V_{BB} = 5 \text{ V}$			
t <sub>s</sub> t <sub>f</sub>	Turn-off storage time Turn-off fall time	- 44	0.51 50	0.7 70	μs ns
	Switching times (inductive load)	$I_{Con} = 1.0 \text{ A}; I_{Bon} = 200 \text{ mA}; L_B = 1 \mu\text{H}; -V_{BB} = 5 \text{ V}; T_i = 100 ^{\circ}\text{C}$			
t <sub>s</sub>	Turn-off storage time		-	1.4	μs
t <sub>ŕ</sub>	Turn-off fall time		-	130	ns

<sup>1</sup> Measured with half sine-wave voltage (curve tracer).

# Silicon Diffused Power Transistor

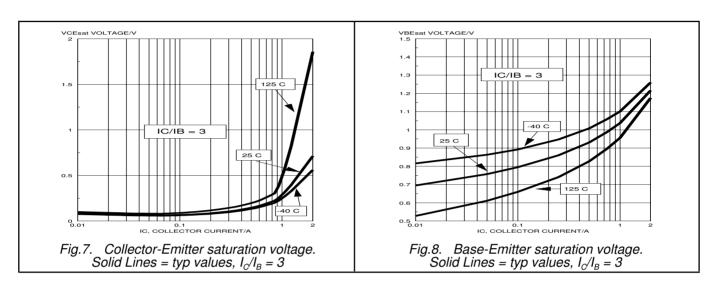
# BUJ100



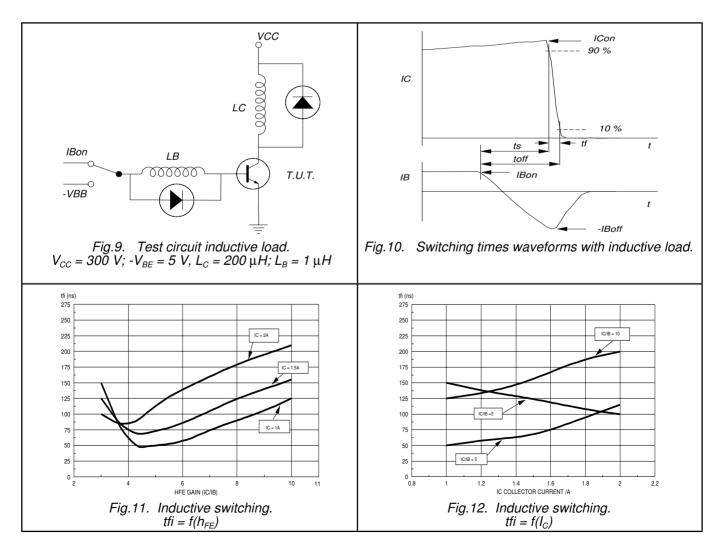
Product specification

## Silicon Diffused Power Transistor

# BUJ100

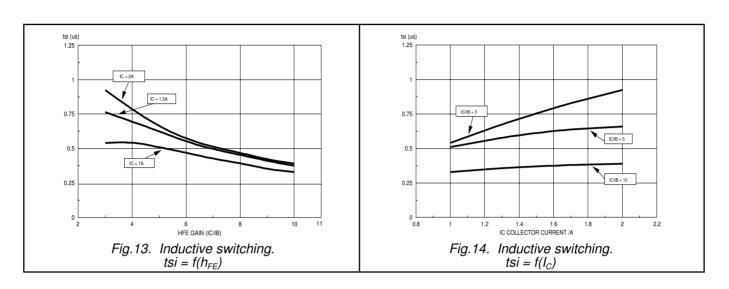


**INDUCTIVE SWITCHING** 

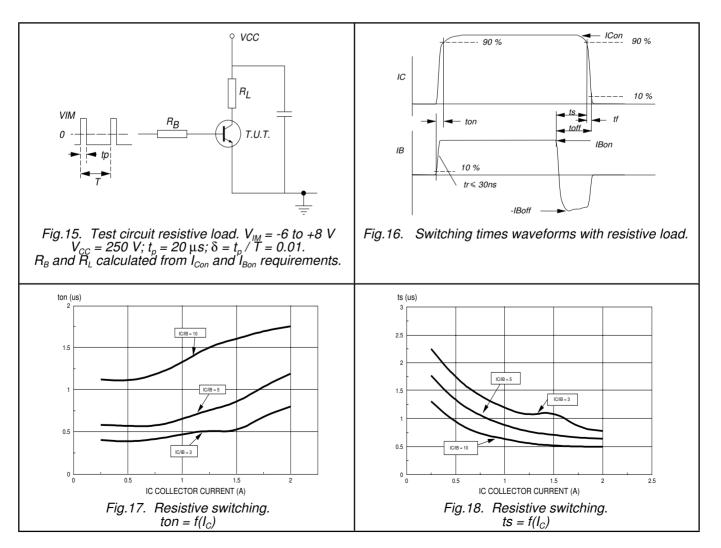


## Silicon Diffused Power Transistor

# BUJ100



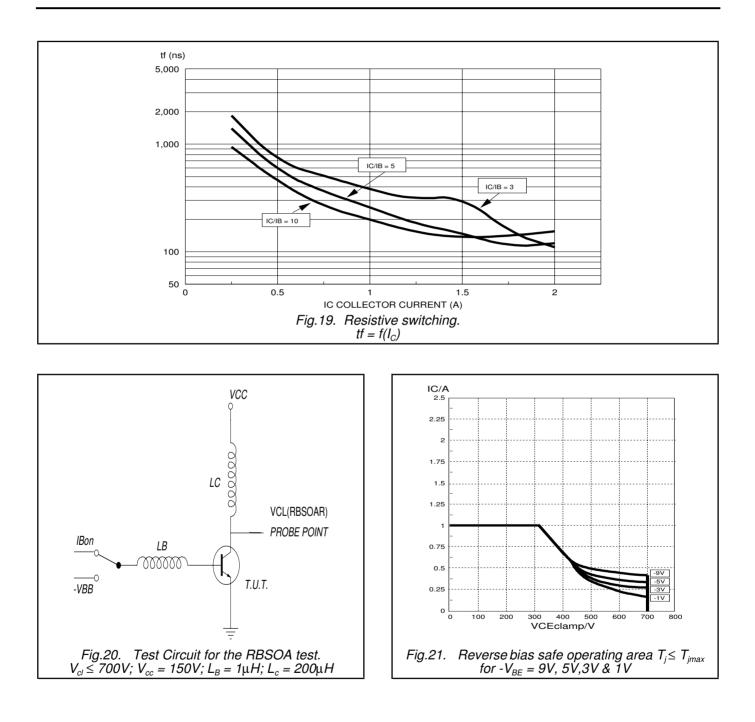
**RESISTIVE SWITCHING** 



Product specification

## Silicon Diffused Power Transistor

# BUJ100

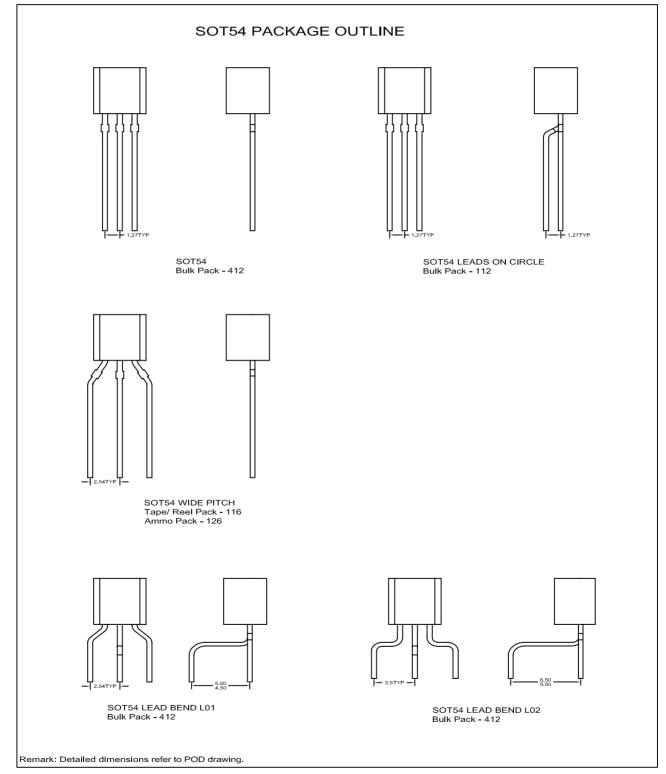


Product specification

## Silicon Diffused Power Transistor

# BUJ100

## **MECHANICAL DATA**



## Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <u>http://www.ween-semi.com</u>.

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